

PATENT  
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UNITED STATES PATENT APPLICATION

OF

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FOR

ASSYMMETRIC DISTRIBUTED BRAGG REFLECTOR FOR VERTICAL CAVITY  
SURFACE EMITTING LASERS

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**CROSS-REFERENCE TO RELATED APPLICATIONS**

[001] Not applicable.

**BACKGROUND OF THE INVENTION**

5 **Field of the Invention**

[002] The present invention relates to laser mirror structures. More specifically, it relates to mirror structures suitable for use in vertical cavity surface emitting lasers.

10 **Discussion of the Related Art**

[003] Vertical cavity surface emitting lasers (VCSELs) represent a relatively new class of semiconductor lasers. While there are many variations of VCSELs, one common characteristic is that they emit light perpendicular to a wafer's surface. Advantageously, VCSELs can be formed from a wide range of material systems to produce specific characteristics. In particular, the various material systems can be tailored to produce different laser wavelengths, such as 1550 nm, 1310 nm, 850 nm, 670 nm, and so on.

[004] VCSELs include semiconductor active regions, distributed Bragg reflector (DBR) mirrors, current confinement structures, substrates, and contacts. Because of their complicated structure, and because of their material requirements, VCSELs are usually grown using metal-organic chemical vapor deposition (MOCVD) or molecular beam epitaxy (MBE).

[005] Figure 1 illustrates a typical VCSEL 10. As shown, an n-doped gallium arsenide (GaAs) substrate 12 has an n-type electrical contact 14. An n-doped lower

mirror stack 16 (a DBR) is on the GaAs substrate 12, and an n-type graded-index lower spacer 18 is disposed over the lower mirror stack 16. An active region 20, usually having a number of quantum wells, is formed over the lower spacer 18. A p-type graded-index top spacer 22 is disposed over the active region 20, and a p-type top mirror stack 24 (another DBR) is disposed over the top spacer 22. Over the top mirror stack 24 is a p-type conduction layer 9, a p-type GaAs cap layer 8, and a p-type electrical contact 26.

[006] Still referring to Figure 1, the lower spacer 18 and the top spacer 22 separate the lower mirror stack 16 from the top mirror stack 24 such that an optical cavity is formed. As the optical cavity is resonant at specific wavelengths, the mirror separation is controlled to resonant at a predetermined wavelength (or at a multiple thereof). At least part of the top mirror stack 24 includes an insulating region 40 that provides for current confinement. The insulating region 40 is usually formed either by implanting protons into the top mirror stack 24 or by forming an oxide layer. The insulating region 40 defines a conductive annular central opening 42 that forms an electrically conductive path through the insulating region 40.

[007] In operation, an external bias causes an electrical current 21 to flow from the p-type electrical contact 26 toward the n-type electrical contact 14. The insulating region 40 and the conductive central opening 42 confine the current 21 such that it flows through the conductive central opening 42 to the active region 20. Some of the electrons in the current 21 are converted into photons in the active region 20. Those photons bounce back and forth (resonate) between the lower mirror stack 16 and the top mirror stack 24. While the lower mirror stack 16 and the top mirror

stack 24 are very good reflectors, some of the photons leak out as light 23 that travels along an optical path. Still referring to Figure 1, the light 23 passes through the p-type conduction layer 9, through the p-type GaAs cap layer 8, through an aperture 30 in the p-type electrical contact 26, and out of the surface of the vertical cavity surface emitting laser 10.

[008] It should be understood that Figure 1 illustrates a typical VCSEL, and that numerous variations are possible. For example, the dopings can be changed (say, by providing a p-type substrate 12), different material systems can be used, operational details can be tuned for maximum performance, and additional structures, such as tunnel junctions, can be added.

[009] While generally successful, VCSELs have problems. In particular, in some applications the available prior art distributed Bragg Reflectors (DBR) are significantly less than optimal. To understand why this is so it is beneficial to consider DBRs in more detail.

[0010] In many applications a DBR must be both highly reflective and highly electrically conductive. In fact, when used in a VCSEL a DBR must be particularly reflective so as to reduce optical losses to such a degree that laser operation is enabled. Reflectivity is achieved by stacking material layers having significantly different indexes of refraction, for example, by stacking alternating layers of AlAs and GaAs. Such stacked layers can produce an optical standing wave within the DBR.

[0011] While the optical performance of stacked AlAs and GaAs is very good, an abrupt junction between an AlAs layer and a GaAs layer would form a high barrier

to current flow. To reduce that barrier, the layers of AlAs and GaAs are joined using a transition region in which the material composition gradually changes from AlAs to GaAs. Furthermore, in most VCSELs the DBR layers are doped to provide free carriers that reduce electrical resistance. The result is a structure that, ideally, has high reflectivity combined with both low optical absorption and low electrical resistance.

[0012] In practice, optical absorption increases with increasing electric field strength, increasing wavelength, and increasing doping levels. Furthermore, p-type dopants tend to have higher optical absorption than n-type dopants. On the other hand, electrical resistance is relatively unaffected by electrical field strength and optical wavelength, yet decreases with increasing doping levels. But, p-type carriers (holes) have much lower mobilities than n-type carriers (electrons). Therefore, as the wavelength increases, such as with VCSELs that output light at 1300, 1310, or 1550 nm, obtaining both low optical absorption and low electrical resistance is difficult. This is because long optical wavelengths tend to be highly absorbed by the free carriers that reduce the electrical resistance. Such is particularly true in top DBRs, which are usually p-doped. The lower mobility of the p-carriers and the higher optical absorption of p-dopants tend to reduce the performance of such top DBRs. Therefore, a conflict occurs in prior art long wavelength VCSELs: to reduce electrical resistance the free carrier concentration should be high, but to reduce light absorption the free carrier concentration should be low.

[0013] In addition, it should be understood that DBRs produce optical standing waves that are characterized by spatially dependent electrical fields. That is, the electric field strength varies across the DBR's thickness. Additionally, the

materials that form a DBR strongly impact on the thermal characteristics of a VCSEL. Binary phase materials, such as AlAs and GaAs tend to have very good thermal conductivity. Thus, heat flows across AlAs and GaAs stacks very well. However, the transition region, which is characterized by three materials, has a significantly lower thermal conductivity. This is because the crystalline structure of the transition region is highly distorted, which reduces thermal conductivity.

[0014] Because of the foregoing, DBRs used in prior art VCSELs have problems with excessive optical absorption, relatively poor thermal conductivity, and relatively high electrical resistance, particularly when producing long wavelength light. Therefore, a new distributed Bragg reflector that has relatively low light absorption and relatively low electrical resistance, particularly at long wavelengths and in top DBRs, would be beneficial. Even more beneficial would be a new distributed Bragg reflector that has relatively low light absorption, relatively low electrical resistance, and relatively good thermal conductivity, particularly at long wavelengths and in top DBRs.

#### **SUMMARY OF THE INVENTION**

[0015] The following summary of the invention is provided to facilitate an understanding of some of the innovative features unique to the present invention, and is not intended to be a full description. A full appreciation of the various aspects of the invention can be gained by taking the entire specification, claims, drawings, and abstract as a whole.

[0016] Accordingly, the principles of the present invention are directed to a new distributed Bragg reflector (DBR) having low light absorption and low electrical resistance. Beneficially, such a DBR is implemented in a manner that results in good thermal conductivity.

5 [0017] A distributed Bragg reflector according to the principles of the present invention is comprised of stacked material layers having different indexes of refraction that are joined by asymmetrical transition regions in which the transition steps within a transition region have different material compositions, different doping levels, and different layer thicknesses. Furthermore, the adjacent transition regions  
10 having transition steps with different thicknesses and different doping levels. Beneficially, adjacent transition regions have different overall thicknesses, with the thinner transition regions being relatively highly doped and located where the optical standing wave within the DBR has low field strength, and with the thicker transition regions being relatively lightly doped and located where the optical standing wave has  
15 a relatively high field strength. The thinner transition regions improve both electrical and thermal conductivity.

[0018] Beneficially, the stacked material layers are comprised of alternating layers of AlAs and GaAs. Even more beneficially, the stacked material layers form a p-doped top mirror of a VCSEL, such as a long wavelength VCSEL.

20 [0019] The novel features of the present invention will become apparent to those of skill in the art upon examination of the following detailed description of the invention or can be learned by practice of the present invention. It should be understood, however, that the detailed description of the invention and the specific

examples presented, while indicating certain embodiments of the present invention, are provided for illustration purposes only because various changes and modifications within the spirit and scope of the invention will become apparent to those of skill in the art from the detailed description of the invention and claims that follow.

**BRIEF DESCRIPTION OF THE DRAWING**

[0020] The accompanying figures, in which like reference numerals refer to identical or functionally-similar elements throughout the separate views and which are incorporated in and form part of the specification, further illustrate the present invention and, together with the detailed description of the invention, serve to explain the principles of the present invention.

[0021] In the drawings:

[0022] Figure 1 illustrates a typical vertical cavity surface emitting laser;

[0023] Figure 2 illustrates a vertical cavity surface emitting laser according to the principles of the present invention; and

[0024] Figure 3 illustrates a distributed Bragg reflector having a standing optical wave, with that distributed Bragg reflector being in accord with the principles of the present invention.

[0025] Note that in the drawings that like numbers designate like elements. Additionally, for explanatory convenience the descriptions use directional signals such as up and down, top and bottom, and lower and upper. Such signals, which are derived from the relative positions of the elements illustrated in the drawings, are meant to aid the understanding of the present invention, not to limit it.



**DETAILED DESCRIPTION OF THE ILLUSTRATED EMBODIMENTS**

[0026] Reference will now be made in detail to an embodiment of the present invention, example of which is illustrated in the accompanying drawings.

[0027] The principles of the present invention provide for asymmetrical distributed Bragg reflectors (DBRs), and for VCSELs that use asymmetrical DBRs.

[0028] Refer now to Figure 2 for an illustration of a vertical cavity surface emitting laser (VCSEL) 100 that is in accord with the principles of the present invention. Figure 2 should be understood as a simplified “cut-away” schematic depiction of a VCSEL that is generally configured as shown in Figure 1. Thus, similar element numbers will be used for similar elements in Figure 2 as were used in Figure 1. However, the VCSEL 100 includes novel and useful top and bottom distributed Bragg reflectors (DBRs).

[0029] As shown in Figure 2, the VCSEL 100 includes an n-doped gallium arsenide (GaAs) substrate 12 having an n-type electrical contact 14. An n-doped lower mirror stack 160 (a DBR) is on the GaAs substrate 12, and an n-type graded-index lower spacer 18 is disposed over the lower mirror stack 160. The lower mirror stack 160 is described in more detail subsequently.

[0030] An active region 20 having P-N junction structures with a number of quantum wells is formed over the lower spacer 18. The composition of the active region 20 is beneficially AlGaAs, with the specific aluminum content varying in the different layers that form the active region 20. For example, one layer may have between twenty and thirty percent of aluminum, while an adjacent layer might have

between zero and five percent of aluminum. There could be many alternating layers, depending how the quantum wells are to be in the active region 20.

[0031] On the active region 20 is a p-type graded-index top spacer 22. A p-type top mirror stack 240 (another DBR) is disposed over the top spacer 22. The upper mirror stack 240 is described in more detail subsequently.

[0032] Over the top mirror stack 240 is a p-type conduction layer, a p-type GaAs cap layer, and a p-type electrical contact, all of which are designated as 260. As in the VCSEL 10 (see Figure 1), the lower spacer 18 and the top spacer 22 separate the lower mirror stack 160 from the top mirror stack 240 such that an optical cavity that is resonant at a specific wavelength is formed.

[0033] Still referring to Figure 2, the top mirror stack 240 includes an insulating region 140, which is beneficially produced by forming the top mirror stack 240 with an AlGaAs layer(s) having a high aluminum content (97-98%), and then oxidizing that high aluminum content layer along an annular ring. Oxidation then produces the oxide insulating region 140.

[0034] In operation, an external bias causes an electrical current 21 to flow into the VCSEL 100. The insulating region 140 forms a current guide into the active region 20 where some of the electrons are converted into photons. Those photons bounce back and forth (resonate) between the lower mirror stack 160 and the top mirror stack 240. While the lower mirror stack 160 and the top mirror stack 240 are very good reflectors, some of the photons leak out as light 23 that travels along an optical path. Still referring to Figure 2, the light 23 passes through an opening 300 in

the element 260 and out of the surface of the vertical cavity surface emitting laser 100.

[0035] The VCSEL 100 of Figure 2 significantly differs from the VCSEL 10 of Figure 1 by asymmetry in the top mirror stack 240 and in the lower mirror stack 160. Those mirror stacks are beneficially formed from layers of different compositions of  $\text{Al}_x\text{Ga}_{1-x}\text{As}$ . While the principles of the present invention are particularly suitable for use in p-doped top mirror stacks (because p-doped materials are optically more absorbent, those materials benefit more from the present invention), those principles are suitable for use with either type of doping. Furthermore, while the illustrated embodiment is based on the  $\text{Al}_x\text{Ga}_{1-x}\text{As}$  material system, the principles of the present invention are applicable to a wide range of compound semiconductor material systems.

[0036] Figure 3 illustrates a distributed Bragg reflector 400 that is suitable for use as either (or both) the lower mirror stack 160 or the top mirror stack 240. In operation, a standing wave is developed within the Distributed Bragg Reflector. A normalized power waveform 402 of such a standing wave, as computed by an optical simulation of a particular embodiment of this invention, is superimposed on the distributed Bragg reflector 400. Figure 3 also illustrates relative thicknesses of the individual layers that comprise the distributed Bragg reflector 400, with the relative thickness of each layer being indicated by the space between vertical lines. As shown in Figure 3, the distributed Bragg reflector 400 includes at least layers 280 through

320. In practice there can be multiple sets of such layers (thus a given distributed Bragg reflector could be comprised of hundreds of individual layers).

[0037] Still referring to Figure 3, the closest vertical line spacing occurs where the electric field is smallest (close to zero), say around layer 300. This indicates the relatively small thicknesses of the layers that form an AlAs-to-GaAs junction (where the composition changes from 100% AlAs to 100% GaAs). The layers that form that junction have relatively high doping levels, which decrease the electrical resistance across the AlAs-to-GaAs transition region. However, because the electric field is low in that transition region, the optical absorption is also low, despite the high doping levels.

[0038] However, still referring to Figure 3, where the electric field is high (say around layer 290) the vertical line spacing is the relatively large, which represents relatively thick individual layers. This represents an interface junction where the GaAs (0% Al) composition changes to AlAs (0% Ga). That area is relatively lightly doped to help to decrease optical absorption of the locally high electric field. The relatively large thickness of the GaAs-to-AlAs transition region helps to decrease the electrical resistance without significantly increasing the optical absorption.

[0039] Thus, a DBR that is in accord with the principles of the present invention is asymmetric in both the interface junction thickness and in the interface junction doping profile. The use of a coupled doping profile and composition profile also contributes to the improved performance of this invention.

[0040] An embodiment of an asymmetric p-doped DBR for use at 1310 nm in the  $\text{Al}_x\text{Ga}_{1-x}\text{As}$  material system having a 0% to 100% Al composition range is

provided for in Table 1. Specific layer thicknesses, doping concentrations, and material compositions are provided. The first interface junction in Table 1 is the AlAs (100% Al composition) to GaAs (0% Al composition) heterojunction, consisting of the layers in row 1 through row 10. Similarly, the layers of the second interface junction, the GaAs (0% Al composition) to AlAs (100% Al composition) heterojunction, are listed in row 13 through row 22.

[0041] The asymmetry of this embodiment can be seen by comparing the total thickness of the first interface junction, 20 nm, with that of the second interface junction, 40 nm. This asymmetry is also seen as the steeper average slope of the leftmost, downward transition in material composition in Figure 4 as compared with the less steep average slope of the rightmost, upward transition. Note that the doping concentration of the first interface junction ranges from  $2.86\text{E}+18\text{ cm}^{-3}$  to  $6.17\text{E}+18\text{ cm}^{-3}$ , while that of the second interface junction ranges from  $6.65\text{E}+17\text{ cm}^{-3}$  to  $1.93\text{E}+18\text{ cm}^{-3}$ . The asymmetry in the interface junction thickness and the asymmetry in doping range work together to provide a lower optical absorption and lower electrical resistance than either separately.

[0042]

Table 1

Layer	Thickness (nm)	Dopant Concentration (cm <sup>-3</sup> )	Composition (% Al)
1	2.50	6.17E+18	99
2	2.50	6.42E+18	94
3	2.00	6.22E+18	84
4	1.50	5.59E+18	71
5	1.50	4.79E+18	52
6	1.50	4.22E+18	42
7	1.50	3.44E+18	29
8	2.00	2.88E+18	15
9	2.50	2.61E+18	5
10	2.50	2.86E+18	1
11	33.99	2.86E+18	0
12	33.99	6.65E+17	0
13	5.00	6.65E+17	1
14	5.00	3.42E+17	5
15	4.00	3.80E+17	15
16	3.00	6.94E+17	0.29
17	3.00	1.11E+18	0.42
18	3.00	1.37E+18	52
19	3.00	1.90E+18	71
20	4.00	2.23E+18	84
21	5.00	2.28+18	94
22	5.00	1.93+18	99
23	36.71	1.93+18	100
24	36.71	6.17+18	100

There are circumstances for which a 0% to 100% range in material composition variation is undesirable. Therefore, Table 2 provides another embodiment DBR that is in accord with the principles of the present invention. That DBR is a p-doped Al<sub>x</sub>Ga<sub>(1-x)</sub>As DBR suitable for use in a 1310 nm VCSEL. That DBR has a restricted Al composition range of 0% to 85%.

Table 2

Layer	Thickness (nm)	Dopant Concentration (cm <sup>-3</sup> )	Composition (% Al)
1	2.50	4.77E+18	84
2	2.50	4.96E+18	80
3	2.00	4.80E+18	71
4	1.50	4.32E+18	60
5	1.50	3.70E+18	44
6	1.50	3.26E+18	36
7	1.50	2.66E+18	25
8	2.00	2.23E+18	13
9	2.50	2.01E+18	4
10	2.50	2.21E+18	1
11	33.99	2.21E+18	0
12	33.99	7.00E+17	0
13	5.00	7.00E+17	1
14	5.00	3.60E+17	4
15	4.00	4.00E+17	13
16	3.00	7.30E+17	25
17	3.00	1.17E+18	36
18	3.00	1.44E+18	44
19	3.00	2.00E+18	60
20	4.00	2.35E+18	71
21	5.00	2.40E+18	80
22	5.00	2.03E+18	84
23	36.71	2.03E+18	85
24	36.71	4.77E+18	85

The two-fold asymmetry of total interface junction thickness and doping concentration range of this embodiment is apparent from Table 2. Furthermore, comparing the rows of Table 2 with the corresponding rows of Table 1 shows that the doping concentration ranges differ in addition to the material composition ranges. Thus, the best embodiments of this invention may vary in many ways, including the number of steps, the individual step size, the total heterojunction thickness, the doping concentration range and the material composition range.

[0043] The embodiments and examples set forth herein are presented to best explain the present invention and its practical application and to thereby enable those skilled in the art to make and utilize the invention. Those skilled in the art, however, will recognize that the foregoing description and examples have been presented for the purpose of illustration and example only. Other variations and modifications of the present invention will be apparent to those of skill in the art, and it is the intent of the appended claims that such variations and modifications be covered. The description as set forth is not intended to be exhaustive or to limit the scope of the invention. Many modifications and variations are possible in light of the above teaching without departing from the spirit and scope of the following claims. It is contemplated that the use of the present invention can involve components having different characteristics. It is intended that the scope of the present invention be defined by the claims appended hereto, giving full cognizance to equivalents in all respects.